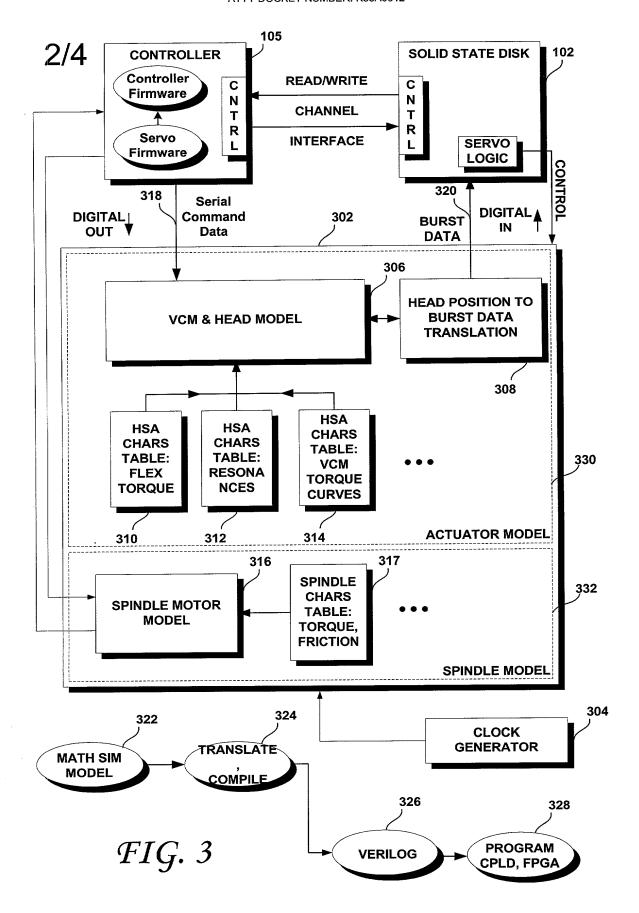


FIG. 2



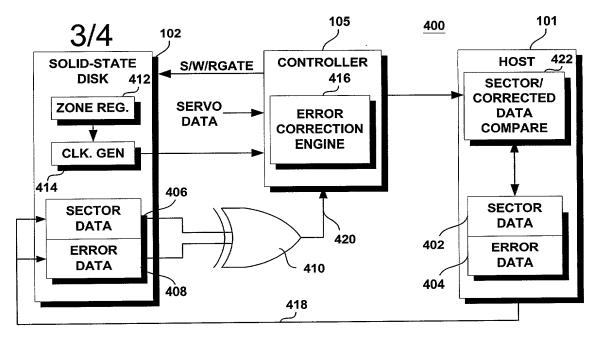
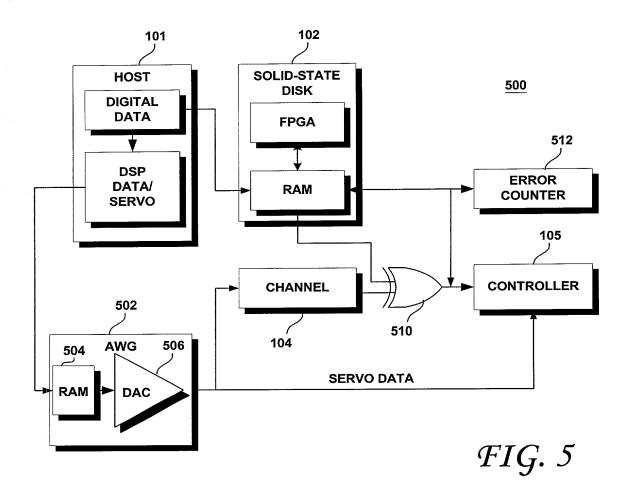


FIG. 4



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**EMULATING READING AND WRITING OF DATA IN** READ/WRITE CHANNEL BASED UPON MODEL OF **READ/WRITE CHANNEL** 

EMULATING BEHAVIOR OF HEAD ACTUATOR **DURING TRACK SEEK AND TRACK FOLLOWING OPERATIONS BASED UPON ELECTROMECHANICAL MODEL OF HEAD ACTUATOR** 

PROVIDING A DISK CONTROLLER DESIGN BASE FOR DEFINING INTEGRATED CIRCUIT ELEMENTS COMPRISING THE DISK CONTROLLER CIRCUIT

PROVIDING A CONTROLLER ENVIRONMENT TO SUPPORT EXECUTION AND DEBUG OF FIRMWARE FOR OPERATING THE DISK CONTROLLER CIRCUIT

PERFORMING A PLURALITY OF DISK FUNCTIONS ACCORDING TO A SCRIPT, THE DISK FUNCTIONS INCLUDING INTERACTIONS OF THE READ/WRITE MODEL, THE ELECTROMECHANICAL MODEL OF HEAD ACTUATOR, THE DISK CONTROLLER DESIGN BASE AND THE CONTROLLER **ENVIRONMENT** 

**S64** 

**S65** 

**S61** 

**S62** 

**S63** 

FIG. 6